

System Construction Course 2015,

Assignment 4

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Introduction

Minos supports preemptive scheduling under certain preconditions that were discussed in the lectures. Background tasks execute in a round robin fashion in the background. Periodic tasks can preempt background tasks. The command scheduler of Minos is an example of a background task. We can protect ourselves from commands executing an infinite loop with a watchdog.

Lessons to Learn

- Learn to know the task scheduling mechanism of Minos.
- Understand and apply the mechanism of a watchdog.

Preparation

- 1. Update your repository
- Copy new and modified files / directories from the Work folder located in (repo)/a2/ to your work directory.
- 3. Make sure you have a proper kernel running on your RPI. You can then use module loading and are not required to link the kernel again for the rest of this exercise.

1 Watchdog and Tasks

Module RPI.Kernel.Mos contains procedures to setup the (largely undocumented) ARM watchdog registers WD0G (Watchdog) and RSTC (Reset Configuration). Bits 0 to 19 of the WD0G register provide a countdown value that, once it hits 0 will make the system reboot, provided register RSTC is set up accordingly. The frequency of the countdown value is 2^{16} Hz, thus a maximum of 16 seconds can be set for the watchdog countdown. Please refer to the implementation of Kernel.StartWatchdog in order to understand the semantics.

Once the countdown is activated, software must periodically update the WDOG register in order to prevent a reboot. In the case of a failed program, the watchdog is no longer updated which results in a reboot of the system.

- 1. Write a program that enables the hardware watchdog.
- 2. Install a background task that (periodically) resets the watchdog.
- 3. Test the watchdog by executing an infinite loop as a command.
- 4. What happens when you install the watchdog resetter as a periodic task? What does it test?

The starting point for this exercise is provided as Assignment4/Assignment.Tool.

Documents

 System Construction Lecture 4 slides from the course-homepage http://lec.inf.ethz.ch/syscon